



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/615,287  
Filing Date ..... July 7, 2003  
Confirmation No. .... 8293  
Inventor ..... Kunal R. Parekh et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2812  
Examiner ..... H. Jey Tsai  
Attorney's Docket No. .... MI22-2260  
Customer No. .... 021567  
Title ..... Methods of Forming Memory Circuitry

**RESPONSE TO MARCH 16, 2004 OFFICE ACTION**

To: Mail Stop Amendment  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**VIA U.S. EXPRESS MAIL**

From: Mark Matkin (Tel. 509-624-4276; Fax 509-838-3424)  
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Responsive to the Office Action dated March 16, 2004, Applicant amends and remarks as follows:

**AMENDMENTS**